

WF-3000 BIPOLAR SYSTEM DEVELOPMENT SET

The Intel WF-3000 Bipolar System Development Set contains the following members of the Schottky Bipolar LSI Microcomputer Set:

- (2) 3001 Microprogram Control Units
- (10) 3002 Central Processing Elements
- (10) 3601 Bipolar PROMs (256 x 4)

Includes all Computing Elements and High Speed Memory required for the construction of 16, 18, or 20 bit processors and/or High Speed Controllers.

N-Bit Word Expandable.

Multi-bus Organization.

High Performance

MCU Cycle Time — 700ns

CPE Cycle Time — 100ns

Total System Cycle Time — 150ns*

*Guaranteed worst case system cycle time for a 16-bit processor with a fast carry (3003) CP array, 3601-1 PROM Memory and a pipelined architecture.

A unique technology updating program insures that all set owners are kept abreast of Bipolar Microcomputer Set developments. This service includes priority mailings of additional design aids — application notes, specification sheets, user manuals — and free samples of new family members.

Upon receipt of the Bipolar system Development Registration Card, free samples of the following computing elements will be sent to development set owners:

3003 Look-Ahead Carry Generator

3212 Multi-Mode Latch Buffer

3214 Interrupt Control Unit

3226 Inverting Bi-Directional Bus Driver

In addition, free samples of new computing elements will be provided as they are announced throughout 1975.

